

(12) INTERNATIONAL APPLICATION PUBLISHED UNDER THE PATENT COOPERATION TREATY (PCT)

(19) World Intellectual Property Organization  
International Bureau



(43) International Publication Date  
21 February 2002 (21.02.2002)

PCT

(10) International Publication Number  
**WO 02/15251 A1**

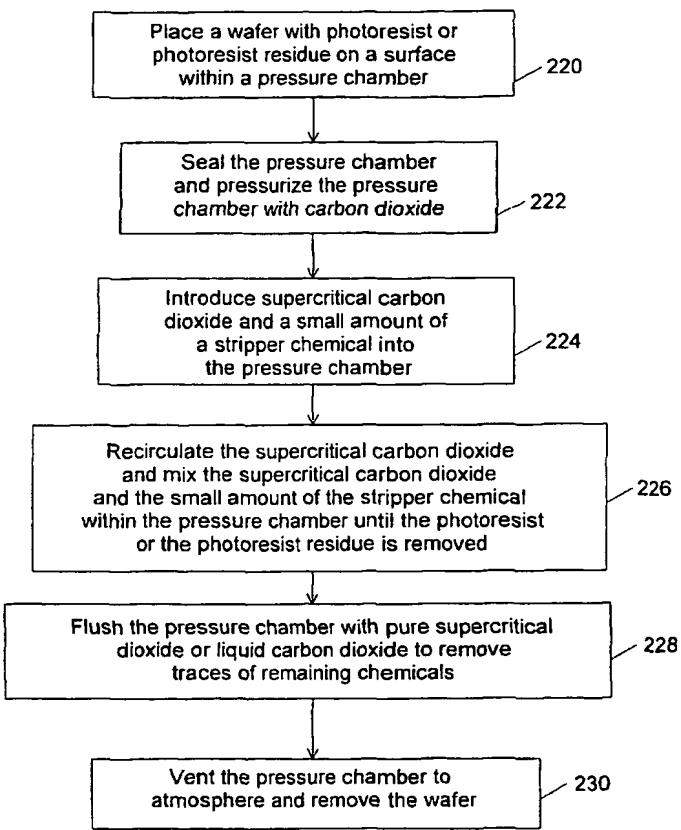
- (51) International Patent Classification?: **H01L 21/311**, 21/3213, 21/306, G03F 7/42, B08B 7/00
- (21) International Application Number: PCT/US00/22454
- (22) International Filing Date: 14 August 2000 (14.08.2000)
- (25) Filing Language: English
- (26) Publication Language: English
- (71) Applicant: TOKYO ELECTRON LIMITED [JP/JP]; Akasaka 5-chome, Minato-ku, Tokyo 107-8481 (JP).
- (72) Inventor: MULLEE, William, H.; 10592 S.W. 63rd Drive, Portland, OR 97219 (US).
- (74) Agents: HAVERSTOCK, Thomas, B. et al.; Haverstock & Owens LLP, Suite 420, 260 Sheridan Avenue, Palo Alto, CA 94306 (US).
- (81) Designated States (national): AE, AL, AM, AT, AU, AZ, BA, BB, BG, BR, BY, CA, CH, CN, CR, CU, CZ, DE, DK, DM, EE, ES, FI, GB, GD, GE, GH, GM, HR, HU, ID, IL, IN, IS, JP, KE, KG, KP, KR, KZ, LC, LK, LR, LS, LT, LU, LV, MA, MD, MG, MK, MN, MW, MX, NO, NZ, PL, PT, RO, RU, SD, SE, SG, SI, SK, SL, TJ, TM, TR, TT, TZ, UA, UG, UZ, VN, YU, ZA, ZW.
- (84) Designated States (regional): ARIPO patent (GH, GM, KE, LS, MW, MZ, SD, SL, SZ, TZ, UG, ZW), Eurasian patent (AM, AZ, BY, KG, KZ, MD, RU, TJ, TM), European patent (AT, BE, CH, CY, DE, DK, ES, FI, FR, GB, GR, IE, IT, LU, MC, NL, PT, SE), OAPI patent (BF, BJ, CF, CG, CI, CM, GA, GN, GW, ML, MR, NE, SN, TD, TG).

Published:

— with international search report

[Continued on next page]

(54) Title: REMOVAL OF PHOTORESIST AND PHOTORESIST RESIDUE FROM SEMICONDUCTORS USING SUPERCRITICAL CARBON DIOXIDE PROCESS



(57) Abstract: A method of removing a photoresist or a photoresist residue from a semiconductor substrate is disclosed. The semiconductor substrate with the photoresist or the photoresist residue on a surface of the semiconductor substrate is placed within a pressure chamber. The pressure chamber is then pressurized. Supercritical carbon dioxide and a stripper chemical are introduced to the pressure chamber. The supercritical carbon dioxide and the stripper chemical are maintained in contact with the photoresist or the photoresist residue until the photoresist or the photoresist residue is removed from the semiconductor substrate. The pressure chamber is then flushed and vented. In an alternative embodiment, supercritical CO<sub>2</sub> carries organic or inorganic chemicals or a combination of the organic and inorganic chemicals into the pressure chamber. The organic or inorganic chemicals or a combination of the organic and inorganic chemicals interact with resist, resist residues, and organic contaminants on the wafer surface and carry these materials and remaining chemicals out of the chamber.

WO 02/15251 A1

CJ

WO 02/15251 A1



*For two-letter codes and other abbreviations, refer to the "Guidance Notes on Codes and Abbreviations" appearing at the beginning of each regular issue of the PCT Gazette.*

**REMOVAL OF PHOTORESIST AND PHOTORESIST RESIDUE  
FROM SEMICONDUCTORS USING SUPERCRITICAL  
CARBON DIOXIDE PROCESS**

**FIELD OF THE INVENTION**

The present invention relates to the field of removal of photoresist and photoresist residue from semiconductor wafers. More particularly, the present invention relates to the field of removal of photoresist and photoresist residue from semiconductor wafers using supercritical carbon dioxide.

**BACKGROUND OF THE INVENTION**

Manufacture of semiconductor devices requires application and subsequent removal of a photoresist chemical from a surface of a semiconductor wafer. The removal of the photoresist chemical, commonly known as stripping, may be immediately preceded by a plasma ashing, etching, or other semiconductor manufacturing step. These steps can degrade or carbonize the photoresist chemical and leave a photoresist residue that is difficult to remove by current stripping methods. The current stripping methods require that the wafers be dipped into baths of commercially available chemical mixtures known as strippers. The baths may employ heat or ultrasonic augmentation. Typically, the baths employ immersion times of twenty to thirty minutes to achieve complete removal of photoresist or photoresist residue from the wafer surface.

What is needed is a more effective method of removing photoresist and photoresist residue.

What is needed is a more efficient method of removing photoresist and photoresist residue.

**SUMMARY OF THE INVENTION**

The present invention is a method of removing a photoresist or a photoresist residue from a semiconductor substrate. The semiconductor substrate with the photoresist or the photoresist residue on a surface of the semiconductor substrate is placed within a pressure chamber. The pressure chamber is then pressurized. Supercritical carbon dioxide and a stripper chemical are introduced into the pressure chamber. The supercritical carbon dioxide and the stripper chemical are maintained in contact with the photoresist or the photoresist residue until the photoresist or the photoresist residue is removed from the semiconductor substrate. The pressure chamber is then flushed and vented.

In an alternative embodiment of the present invention, supercritical CO<sub>2</sub> carries organic or inorganic chemicals or a combination of the organic and inorganic chemicals into the pressure chamber, which is heated and pressurized. The organic or inorganic chemicals or a combination of the organic and inorganic chemicals interacts with resist, resist residues, and organic contaminants on the wafer surface and carries these materials and remaining chemicals out of the chamber.

#### BRIEF DESCRIPTION OF THE DRAWINGS

FIG. 1 is a flow chart illustrating the steps of a method of the present invention.

Fig. 2 is a fragmentary cross-sectional view of a pre-processed semiconductor wafer supporting several material layers.

Fig. 3 is a schematic diagram showing chambers, pipes, and valves of a simplified resist removal system in accordance with the present invention.

Fig. 4 is a flow diagram showing a simplified sequence of process steps of a resist removal system in accordance with the present invention.

Fig. 5 is a fragmentary cross-sectional view of the wafer of Fig. 2 subsequent to a resist removal step in accordance with the present invention.

Fig. 6 presents a table showing a few examples of tests performed to remove photoresist from a wafer.

#### DETAILED DESCRIPTION OF THE PREFERRED EMBODIMENT

The preferred embodiment of the present invention utilizes the high solvency and cleaning characteristics of supercritical carbon dioxide to assist in the stripping process of photoresist or photoresist residue. Only a small fraction of a stripper chemical is required to affect the stripping process compared to the prior art. In the preferred embodiment of the present invention, the supercritical carbon dioxide carries the stripper chemical onto the wafer to be cleaned and is then recycled back to a carbon dioxide compressor for reuse. The stripper chemical is typical of chemicals found in commercially available stripper products. The high degree of solvency and solubilizing ability provided by the supercritical carbon dioxide enhances the removal of the photoresist or the photoresist residue. The high solubilizing ability provided by the supercritical carbon dioxide is well known to science and has been exploited in numerous other applications, for example in cleaning of metal parts.

Solvency of the supercritical carbon dioxide increases with pressure. The supercritical carbon dioxide effectively carries a small amount of the stripper chemical onto sub-micron surface features of modern semiconductor devices because diffusivity and

viscosity of the supercritical carbon dioxide is similar to a gas phase and because density of the supercritical carbon dioxide is nearly equal to a liquid phase. The supercritical carbon dioxide also carries away the photoresist, or the photoresist residue, and remaining stripper chemical from the surface of the wafer. Thus, it is possible to use the small amount of the stripper chemical to perform the stripping process and to also carry away remaining chemicals and residue.

The preferred embodiment of the present invention is illustrated in Fig. 1. A wafer with the photoresist or the photoresist residue is placed in a pressure chamber in a first process step 220. The pressure chamber is sealed and pressurized with carbon dioxide in a second process step 222. As pressure inside the pressure chamber increases, the carbon dioxide becomes liquid and then reaches supercritical temperature and supercritical pressure. Typical process conditions range from 20 to 70 °C and from 1,050 to 6,000 psig.

When desired process conditions are reached, the small amount of the stripper chemical is introduced into a supercritical carbon dioxide stream and thus added into the pressure chamber in a third process step 224. A volume ratio of the stripper chemical to the supercritical carbon dioxide is preferably 0.1 to 15.0 v/v %. The stripper chemical is preferably selected from the group consisting N-methyl pyrrolidone, monoethanol amine, di-isopropyl amine, tri-isopropyl amine, diglycol amine, hydroxyl amine, catechol, and a mixture thereof. Monoethanol amine, hydroxyl amine, and catechol have only marginal utility.

Processing continues with recirculation of the supercritical carbon dioxide and with mixing of the supercritical carbon dioxide and the stripper chemical within the pressure chamber in a fourth process step 226. The fourth process step 226 continues until the photoresist or the photoresist residue is removed from the wafer, typically from 3 to 15 minutes. The pressure chamber is then flushed with pure supercritical carbon dioxide or liquid carbon dioxide to remove traces of the remaining chemicals in a fifth process step 228. Finally, the pressure chamber is vented to atmosphere and the wafer is removed in a sixth process step 230. An optional final process step rinses the wafer with deionized or ultra-pure water.

Use of the supercritical carbon dioxide in combination with the small amount of the stripper chemical greatly enhances the removal of the photoresist, or the photoresist residue, from surfaces of semiconductor devices. The amount of the stripper chemical required to effectively remove the photoresist or the photoresist residue from the wafer is reduced significantly by using supercritical carbon dioxide compared to the prior art wet chemical stripping methods. An amount of hazardous chemical waste generated as a result of using the

supercritical carbon dioxide and the stripper chemical is significantly less than the prior art wet chemical stripping methods. The supercritical carbon dioxide and the stripper chemical eliminates a need for the prior art wet chemical stripping methods along with using large amounts of chemicals and expensive wet baths. Also, the supercritical carbon dioxide and the stripper chemical remove traces of organic contamination from the wafer.

In a first alternative embodiment of the present invention, a small amount of an organic solvent is added to the supercritical carbon dioxide and the stripper chemical. The organic solvent is preferably selected from the group consisting of alcohols, ethers, and glycols. The organic solvent enhances removal of the traces of the organic contamination from the wafer.

Fig. 2 is a fragmentary cross-sectional view of a pre-processed semiconductor wafer 10 supporting a variety of layers. With reference to Fig. 2, semiconductor wafer 10 typically comprises a silicon or ceramic substrate 12 that supports one or more metallic layers 14 that may be protected by one or more alternating passivation or other layers 16. Layers 14 and 16 form an elevationally varied surface 18 that is typically covered with a resist layer 20 and subjected to a photolithographic process to create features 22 (not shown to scale). Conventional features 22, such as vias, line widths, or pitches, may be as small as 0.25  $\mu\text{m}$  and smaller with aspect ratios of depth 24 to width 26 that are greater than 5:1 or greater than or equal to 10:1.

In accordance with the present invention, resist layer 20 may be a remnant from a prior lithographic or other circuit fabrication process and may have subsequently undergone etching, plasma ashing, or semiconductor manufacturing steps. The resist may, therefore, include sidewall polymer residue or carbonaceous residue left after any of these techniques. Alternatively resist layer 20 may be newly applied to protect layers 14 and 16 during a processing operation on the back side of wafer 10, such as during marking, etching, or grinding or as a blanket protection during ion implantation. Thus, skilled persons will appreciate that wafer 10 may be partly or completely covered with a resist material, resist residue, or a contaminant from a subsequent process. The resist material is typically a positive or negative photoresist used for a photolithographic process. Photoresist materials include, but are not limited to Novolak (M-Cresol formaldehyde) or etch-resistant poly coatings such as poly isoprene, poly-(methyl isopropenyl) or etch-resistant poly coatings such as poly isoprene, poly-(methyl isopropenyl ketone) (PMIPK), or polymethyl methacrylate (PMMA). The resist material need not be a photoresist and may comprise any form of resist material with or without photosensitizers.

Fig. 3 is a schematic diagram of a simplified resist removal system 30 of the present invention, and Fig. 4 is a flow diagram of a simplified resist removal process 32 according to the present invention. With the reference to Figs. 3 and 4, removal process 32 is preferably initiated by activating heat exchanger 34 to reduce the temperature of coolant flowing through cold trap 36. Then, system pre-heating step 38 brings pressure vessel 40, including wafer chamber 42, and solvent chambers 44 and 46 to a preferred operating temperature of 45 to 65 °C prior to the arrival of wafer 10. Skilled persons will appreciate that the pressure vessel 40 may alternatively be maintained at a preferred processing temerature to facilitate throughput, or the temperature may be gradually increased to from ambient temperature after wafer 10 enters pressure vessel 40 to reduce stress on wafer 10 or semiconductor devices or features 22 fabricated on wafer 10. Although electrical resistance heaters are preferably built into the walls of vessel 40 and chamber 44 and 46 to perform heating step 38, skilled persons will appreciate that other conventionally available heating techniques could be employed. Skilled persons will also appreciate that that electrical resistance tape may be wrapped around all or some of the connecting lines, such as line 43 between pump 92 and vessel 40 and lines 45 and 47 between respective chambers 44 and 46 and line 43, to maintain the temperature of parts of system 30 at or near the temperature of vessel 40 and chambers 44 and 46.

Wafer placement step 48 employs manual or automatic conventional wafer handling techniques to place one or more wafers 10 into wafer chamber 42 in pressure vessel 40. Wafer(s) may be oriented horizontally or vertically and supported by clips, electrostatic or vacum chucks, or other methods well known to skilled practitioners. Pressure vessel 40 may include one or more wafer airlocks, may comprise of a gasket-mated two-piece vessel with a stationary portion and hydraulically raised and lowered portion, or may be sealed by other mechanisms.

Purging step 50 that purges solvent chambers 44 and 46 and pressure vessel 40 with fluid CO<sub>2</sub>, preferably gaseous CO<sub>2</sub>, preferably begins with all valves 60, 61, 62, 64, 66, 67, 68, 70, 72, 74, 76, 78, 80, 82, 84, 86, 87, and 88 in a closed position. CO<sub>2</sub> tank valve 60 is opened to allow fluid CO<sub>2</sub>, preferably liquid CO<sub>2</sub>, to flow from CO<sub>2</sub> tank 90 to pressure regulating valve 61 that ensures that the pressure is preferably greater than 750 pounds per square inch gauge pressure above ambient atmospheric pressure (psig). Vent valve 62 and pump valve 64 are preferably sequentially opened to allow CO<sub>2</sub> into into pump 92 and through vent 94. Valve 66 allows compressed air from compressed air source 96 to reach pressure regulating valve 67 that is set to ensure a pressure of between 50 to 90 psig, and preferably 60 to 80 psig. CO<sub>2</sub> is allowed to flow out vent 94 by cycling pump 92 for preferably at least five seconds, prior to preferably sequentially opening valves 68, 70, 72,

and 74 to purge solvent chamber 44; sequentially opening valves 76, 78, 80, and 82 to purge solvent chamber 46; and sequentially opening valves 84, 86, 88 and 87 to purge pressure vessel 40 through vent 98, cold trap 36 or exhaust 100.

To accomplish pressure vessel purging step 110, valves 88, 84, 78, 82, 74, and 70 are preferentially sequentially closed. The system pressure is then preferably adjusted between 1,000 and 2,000 psig, and preferably between 1,000 and 1,500 psig by controlling the pumping rate at pressure regulating valve 61 and by adjusting the back pressure regulator 112. Back pressure regulator 112 is positioned between pressure vessel 40 and exhaust 100 and allows line 113 to be depressurized to ambient atmospheric pressure. The CO<sub>2</sub> system flow is also preferably set to between 0.5 and 20 liters per minute (LPM), and more preferably between 3 to 6 LPM.

After valves 70, 74, 78, and 80 are closed, solvents can be introduced in solvent chambers 44 and 46 through respective valves 72 and 80 to accomplish solvent chamber filling up step 114. Valves 72 and 80 are subsequently closed before valves 74 and 82 are opened to allow solvent loops 116 and 118 to achieve the instantaneous system pressure. Skilled persons will appreciate that step 114 can be performed any time after step 50 and before step 122 with the proper sequence of valve control.

Pressurizing system step 120 involves increasing the pressure of CO<sub>2</sub> in the system to between 2,000 and 6,000 psig, more preferably between 2,500 and 4,500 psig, and most preferably between 3,000 and 3,500 psig by adjusting back pressure valve 112. Other generally preferred conditions for the resist removal process of the present invention range from 10 to 80°C and 750 to 6000 psig, and preferably from 40 to 70°C and 1050 to 4500 psig. When the desired conditions are reached, valve 70 is opened and valve 68 is closed in solvent introduction step 122 to force the CO<sub>2</sub> stream to flow through solvent loop 116 and solvent chamber 44 to introduce a small amount of one or more chemicals into the supercritical CO<sub>2</sub> stream and into pressure vessel 40. The CO<sub>2</sub> flow rate may be reduced to 0.5 LPM, for example, to increase the chemical residence time in pressure vessel 40.

In a second alternative embodiment, the preferred types of chemicals include: N-Methyl Pyrrolidone (NMP), diglycol amine, hydroxyl amine, tertiary amines, catechol, ammonium fluoride, ammonium bifluoride, methylacetacetamide, ozone, propylene glycol monoethyl ether acetate, acetylacetone, dibasic esters, ethyl lactate, CHF<sub>3</sub>, BF<sub>3</sub>, other fluorine containing chemicals, or a mixture of any of the above chemicals. Optionally, one or more of these chemicals or mixture of chemicals may be introduced into system 30 as described above from the same or a different solvent chamber(s) 44 and 46. Other chemicals such as an organic solvent may be used independently or added to one or more of the above chemicals to

remove organic contaminants from the wafer surface. The organic solvent may include, for example, and alcohol, ether, and/or glycol, such as acetone, diacetone alcohol, dimethyl sulfoxide (DMSO), ethylene glycol, methanol, ethanol, propanol, or isopropanol (IPA). Although conventionally large amounts of chemicals or mixtures of chemicals in an amount the is less than 15%  $v_c/v_v$ , where  $v_c$  is the liquid volume of the chemical and  $v_v$  is the volume of pressure vessel 40. Preferably less than a few millimeters of chemicals are employed for each resist removal step 130; however, larger amounts can be used.

Resist removal step 130 allows the supercritical CO<sub>2</sub> to carry the solvents into pressure vessel 49 and into contact with the resist, residue, or other contaminants on wafer 10. The supercritical CO<sub>2</sub> can be recirculated through recirculation loop 133 to pressure vessel 49 until resist layer 20 is removed. Cold trap 36 removes chemicals from the depressurized CO<sub>2</sub> gas in line 113, and heat exchanger 34 along loop 133 cools the CO<sub>2</sub> to a liquid before it reaches pump 92. Resist removal step 130 is accomplished in preferably ten seconds to 15 minutes, and more preferably from 30 seconds to ten minutes, and most preferably from 30 seconds to three minutes. Valves 70 and 74 are closed and valve 68 is opened to bypass solvent chamber 44 for closing solvent chamber step 132.

In a third alternative embodiment, a second set of solvent introduction, resist removal, and closing solvent chamber steps 122, 130, and 132 are performed in connection with solvent chamber 46. Valve 78 is opened and valve 76 is closed to force the CO<sub>2</sub> stream to flow through loop 118 and chamber 46 to introduce a second chemical or group of chemicals into the CO<sub>2</sub> stream and into pressure vessel 40. The second resist removal step 130 may employ the same or different chemical(s) employed in the first removal step 130 and and may be conducted for a same or different time period. Then valves 82 and 78 and closed and valve 76 is opened to bypass solvent chamber 46. In a fourth alternative embodiment, valve 136 is closed and valve 87 is open, and each set of steps 122, 130 and 132 is performed in ten seconds to one minute without solvent recirculation. A 2.5 μm-thick resist layer 20 can be removed from the surface of an 6", 8" or 300 mm diameter wafer 10 with two removal steps 130 of less than 30 seconds each. Thus, each wafer 10 or group of wafers 10 can be processed in less than one minute.

Pressure vessel 40 is then flushed for five to thirty seconds, with supercritical CO<sub>2</sub> and/or liquid CO<sub>2</sub> to remove all traces of remaining chemicals. Finally, pressure vessel 40 is depressurized in step 134 by closing valves 66 and 60 and opening valves 62, 74, 82, 84 and 87 to vent the system atmosphere.

Skilled persons will appreciate that system 30 preferably includes one directional check valves 142, 144, 146, 148, 150, and 151 to ensure the direction of flow indicated in the flow

lines of Fig. 3. Skilled persons will also appreciate that system 30 preferably includes pressure gauges 152, 154, 156, 158, 160, 162, and 164 that may be monitored so that pump 92 or back pressure regulating values may be adjusted manually or by computer as needed.

Fig. 5 is a fragmentary cross section view showing wafer 140 following step 134 without resist layer 20. For convenience, certain features of Fig. 5 that correspond to features of Fig. 2 have been designated with the same reference numbers. With reference to Fig. 5, wafer(s) 140 are then preferably removed and rinsed with deionized (DI) or ultra pure water to finish the cleaning process.

Fig. 6 presents a table showing a few examples of tests performed to remove photoresist from a wafer 10. Electron micrographs of wafers 10 subjected to these trials exhibited surface of resulting stripped wafers 140 that were substantially free of photoresist or its residue.

Skilled persons will appreciate that the method of the present invention eliminates the requirement for a carbonizing or ashing process prior to resist removal, substantially reducing the cost, equipment, and process time conventionally needed for resist removal.

Skilled persons will also appreciate that the method of the present invention outperforms conventional resist removal processes to the extent that it eliminates the need for a conventional post-stripping cleaning step such as a "piranha" bath employing hazardous chemicals. Furthermore, the relatively small amount of chemicals utilized by the method of the present invention offers tremendous cost savings over conventional techniques and chemical baths. Finally, the method of the present invention facilitates increased wafer throughput.

It will be readily apparent to one skilled in the art that other various modifications may be made to the preferred embodiment without departing from the spirit and scope of the invention as defined by the appended claims.

## CLAIMS

1. A method of removing a material selected from the group consisting of a photoresist, a photoresist residue, and a combination thereof from a surface of a semiconductor substrate comprising the steps of:
  - a. placing the semiconductor substrate with the material on the surface of the semiconductor substrate within a pressure chamber;
  - b. pressurizing the pressure chamber;
  - c. introducing supercritical carbon dioxide and a stripper chemical into the pressure chamber;
  - d. mixing the supercritical carbon dioxide and the stripper chemical within the pressure chamber until the material is removed from the semiconductor substrate; and
  - e. flushing the pressure chamber.
2. The method of claim 1 wherein the stripper chemical is selected from the group consisting of N-methyl pyrrolidone, di-isopropyl amine, tri-isopropyl amine, diglycol amine, and a mixture thereof.
3. The method of claim 1 wherein a volume ratio of the stripper chemical to the supercritical carbon dioxide is within the range and including 0.1 to 15.0 %.
4. The method of claim 1 further comprising the step of adding an organic solvent to the supercritical carbon dioxide and the stripper chemical within the pressure chamber.
5. The method of claim 4 wherein the organic solvent is chosen from the group consisting of alcohols, ethers, and glycols.
6. The method of claim 1 wherein the photoresist residue is formed in a plasma ashing process.
7. The method of claim 1 wherein the photoresist residue is formed in a plasma etching process.

8. A method of removing a material selected from the group consisting of a photoresist, a photoresist residue, and a combination thereof from a surface of a semiconductor substrate comprising the steps of:
  - a. introducing supercritical carbon dioxide and a stripper chemical to the semiconductor substrate with the material on the surface of the semiconductor substrate;
  - b. maintaining the supercritical carbon dioxide and the stripper chemical in contact with the semiconductor substrate until the material is removed; and
  - c. removing the supercritical carbon dioxide and the stripper chemical from contact with the semiconductor substrate.
9. The method of claim 8 wherein the stripper chemical is selected from the group consisting of N-methyl pyrrolidone, di-isopropyl amine, tri-isopropyl amine, diglycol amine, and a mixture thereof.
10. The method of claim 8 wherein a volume ratio of the stripper chemical to the supercritical carbon dioxide is within the range and including 0.1 to 15.0 %.
11. The method of claim 8 further comprising the step of adding an organic solvent to the supercritical carbon dioxide and the stripper chemical within the pressure chamber.
12. The method of claim 11 wherein the organic solvent is chosen from the group consisting of alcohols, ethers, and glycols.
13. The method of claim 8 wherein the photoresist residue is formed in a plasma ashing process.
14. The method of claim 8 wherein the photoresist residue is formed in a plasma etching process.
15. A method of processing a semiconductor wafer having a surface supporting material, the material selected from the group consisting of a resist, a resist residue, and a combination thereof, the method comprising the steps of:

- a. exposing the material to supercritical CO<sub>2</sub> in combination with a first solvent, the first solvent being selected from the group consisting of n-methyl pyrrolidone, di-isopropyl amine, tri-isopropyl amine, diglycol amine, hydroxyl amine, and a mixture thereof; and
  - b. maintaining the supercritical carbon dioxide and the first solvent in contact with the material until substantially all of the material has been removed from the surface of the wafer.
16. The method of claim 15 further comprising the steps of:
- a. heating a process chamber;
  - b. placing the wafer having the surface supporting material in the process chamber prior to exposing the material to the supercritical carbon dioxide;
  - c. pressurizing the process chamber with CO<sub>2</sub> prior to exposing the material to the supercritical carbon dioxide and the first solvent; and
  - d. flushing the process chamber with the supercritical CO<sub>2</sub> subsequent to exposing the material to the supercritical CO<sub>2</sub> and the first solvent in order to remove the material and the first solvent from the process chamber.
17. The method of claim 16 wherein the process chamber is pressurized to a pressure between about 1050 and 6000 psig.
18. The method of claim 17 wherein the process chamber is pressurized to a pressure between about 2500 and 4500 psig.
19. The method of claim 17 wherein the process chamber is heated to a temperature between about 20 and 80 °C.
20. The method of claim 19 wherein the process chamber is heated to a temperature between about 46 and 70 °C.
21. The method of claim 16 wherein the process chamber is heated to a temperature greater than about 46 °C and less than about 80 °C.
22. The method of claim 15 wherein a ratio of the first solvent to the combination of the supercritical CO<sub>2</sub> and the first solvent is less than 15% by volume.

23. The method of claim 15 further comprising the step of adding a second solvent to the combination of the supercritical CO<sub>2</sub> and the first solvent.
24. The method of claim 23 wherein a ratio of the first and second solvents to the combination of the supercritical carbon dioxide and the first and second solvents is less than 15% by volume.
25. The method of claim 15 further comprising the step of removing organic contaminants from the wafer surface.
26. The method of claim 25 further comprising the step of adding an organic solvent to the combination of the supercritical CO<sub>2</sub> and the first solvent.
27. The method of claim 26 wherein the organic solvent is selected from the group consisting of alcohols, ethers, and glycols.
28. The method of claim 15 wherein a time period for maintaining the supercritical CO<sub>2</sub> and the first solvent in contact with the wafer having the surface supporting material is between 10 seconds and 15 minutes.
29. The method of claim 28 wherein the time period is less than about 5 minutes.
30. The method of claim 29 wherein the time period is less than about one minute.
31. The method of claim 16 further comprising the step of adding liquid CO<sub>2</sub> to the pressure chamber subsequent to pressurizing the process chamber with the CO<sub>2</sub> and prior to introducing the supercritical CO<sub>2</sub> and the first solvent to the pressure chamber.
32. The method of claim 16 wherein the wafer is placed in the process chamber prior to heating the process chamber.
33. The method of claim 15 wherein the wafer surface includes feature sizes of less than 0.5 μm.

34. The method of claim 15 wherein the wafer surface includes one or more vias having a depth to diameter aspect ratio that is greater than 5:1.
35. The method of claim 15 wherein a thickness of the material is greater than 1.5  $\mu\text{m}$ .
36. The method of claim 15 wherein the material comprises a photoresist.
37. The method of claim 15 wherein the material comprises a photoresist residue.
38. The method of claim 15 further comprising the step of adding a 3 to 6 carbon dione to the combination of the supercritical  $\text{CO}_2$  and the first solvent.
39. The method of claim 38 wherein the first solvent includes the n-methyl pyrrolidone.
40. A method of processing a semiconductor wafer having a surface supporting material, the material selected from the group consisting of a resist, a resist residue, and a combination thereof, the method comprising the steps of:
  - a. exposing the material to supercritical  $\text{CO}_2$  in combination with a first solvent at a pressure greater than or equal to 1,050 psig and less than or equal to 6,000 psig, the first solvent being selected from the group consisting of n-methyl pyrrolidone, di-isopropyl amine, tri-isopropyl amine, diglycol amine, hydroxyl amine, and a mixture thereof; and
  - b. maintaining the supercritical carbon dioxide and the first solvent in contact with the material until substantially all of the material has been removed from the surface of the wafer.
41. A method of processing a semiconductor wafer having a surface supporting material, the material selected from the group consisting of a resist, a resist residue, and a combination thereof, the method comprising the steps of:
  - a. exposing the material to supercritical  $\text{CO}_2$  in combination with a first solvent at a temperature greater than or equal to 46 °C and less than or equal to 80 °C, the first solvent being selected from the group consisting of n-methyl

- pyrrolidone, di-isopropyl amine, tri-isopropyl amine, diglycol amine, hydroxyl amine, and a mixture thereof; and
- b. maintaining the supercritical carbon dioxide and the first solvent in contact with the material until substantially all of the material has been removed from the surface of the wafer.
42. A method of processing a semiconductor wafer having a surface supporting material, the material selected from the group consisting of a resist, a resist residue, and a combination thereof, the method comprising the steps of:
- a. exposing the material to supercritical CO<sub>2</sub> in combination with n-methyl pyrrolidone; and
- b. maintaining the supercritical carbon dioxide and the n-methyl pyrrolidone in contact with the material until substantially all of the material has been removed from the surface of the wafer.
43. The method of claim 42 wherein a ratio of the n-methyl pyrrolidone to the combination of the supercritical carbon dioxide and the n-methyl pyrrolidone is between 0.1 and 15% by volume.

1/5

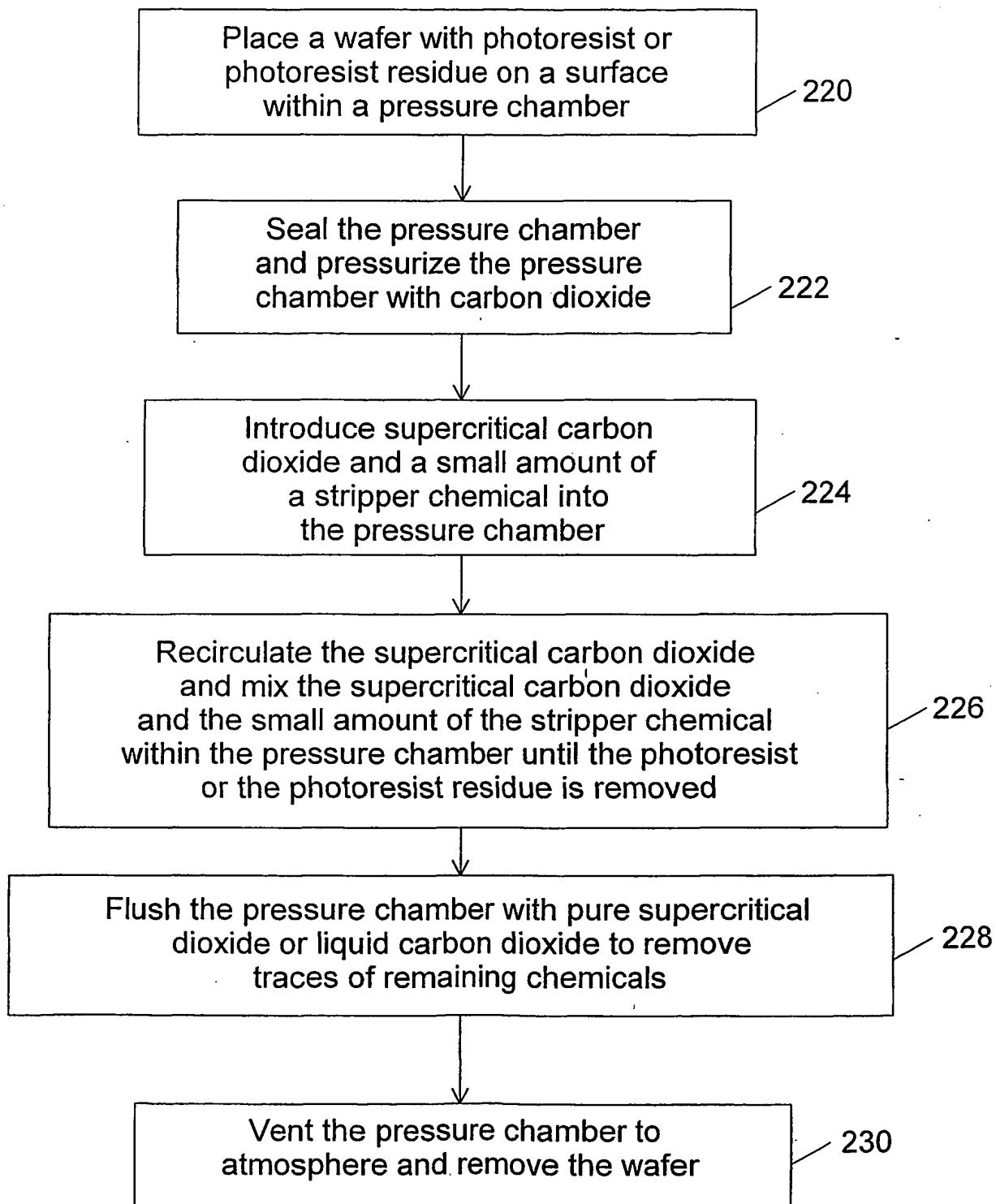


FIG. 1

2/5

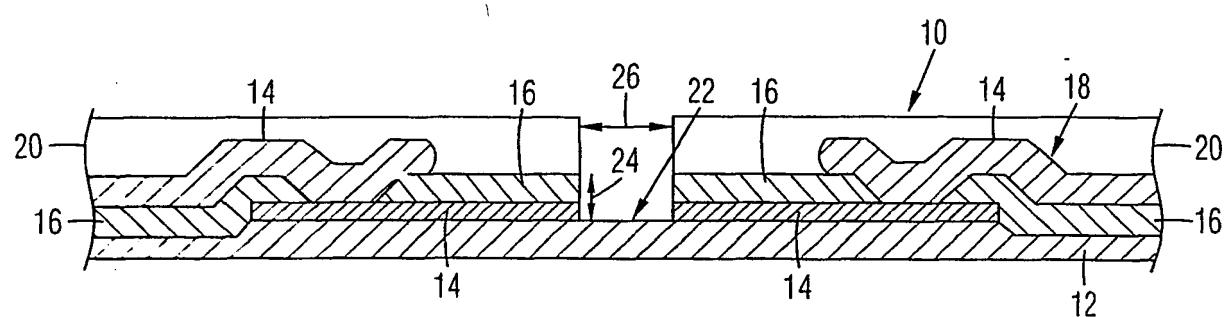


FIG. 2

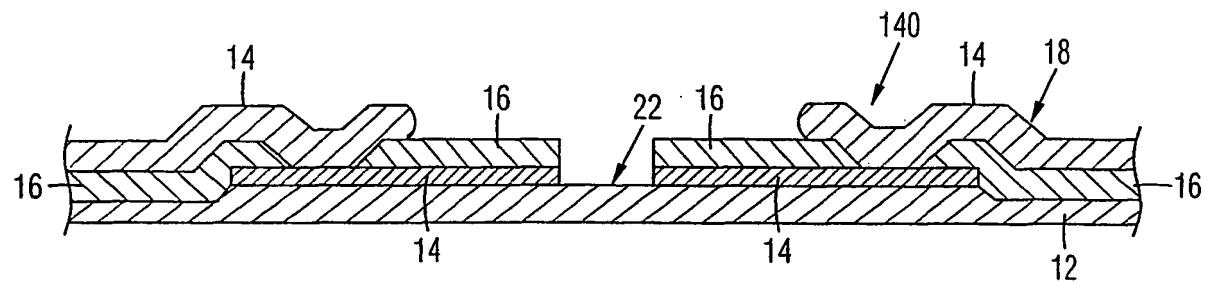
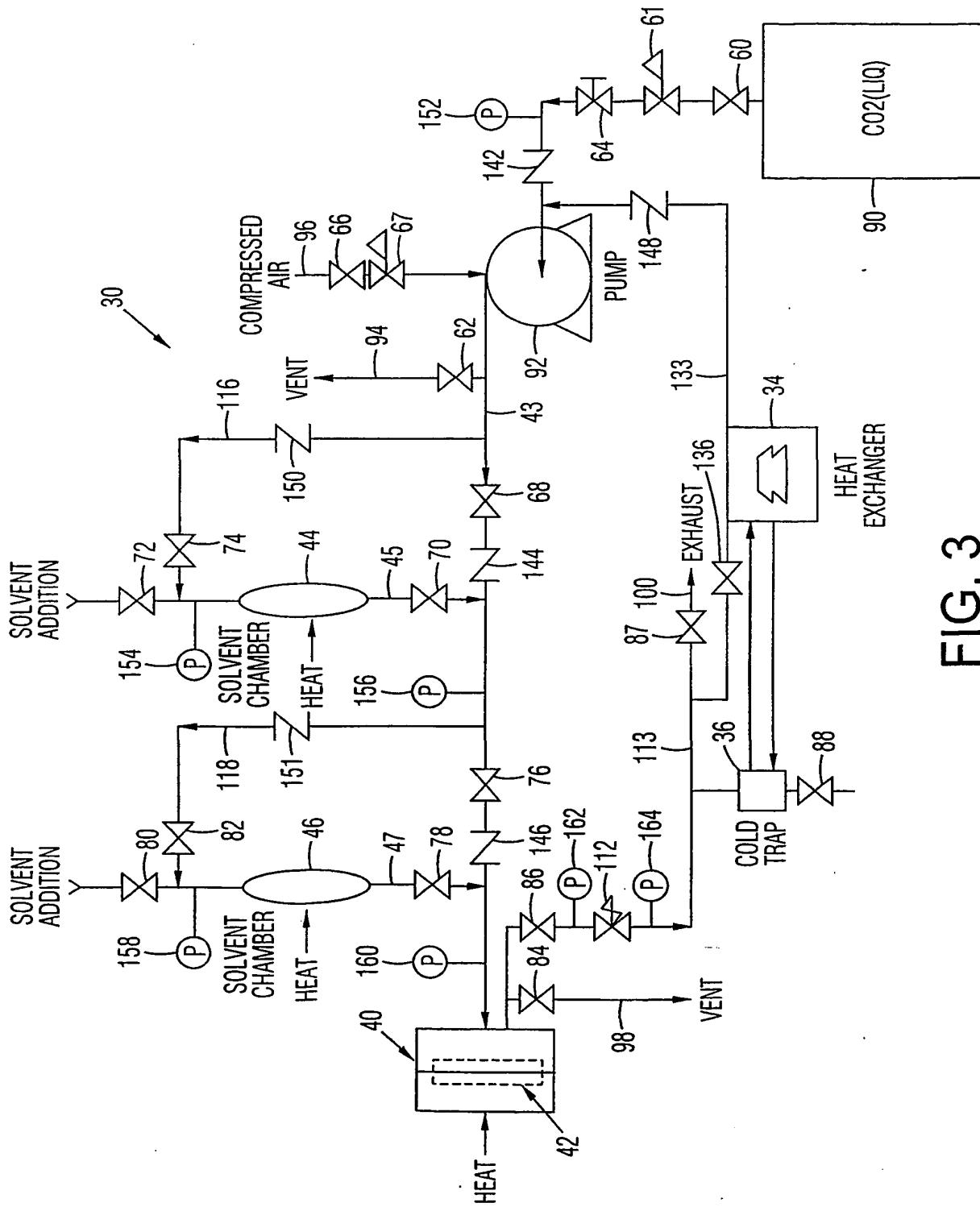


FIG. 5



3  
E/G

4/5

## RESIST REMOVAL PROCESS

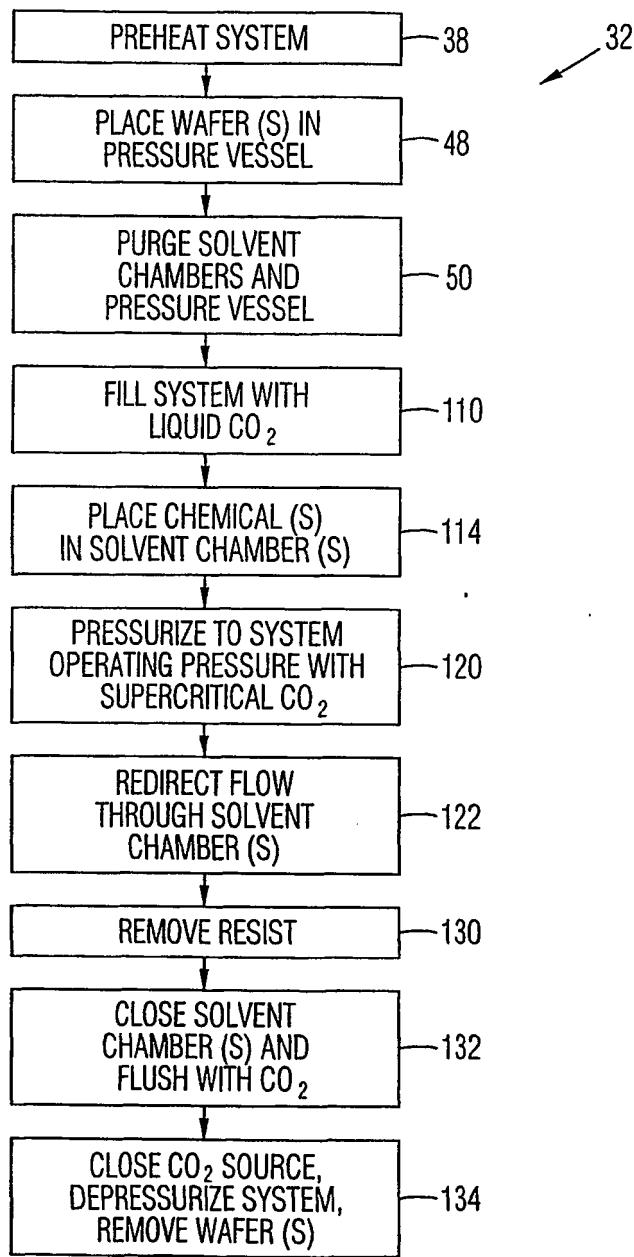


FIG. 4

5/5

SAMPLE NO.	RESIST MATERIAL	DEPTH ON 8" WAFER ( $\mu\text{m}$ )	FEATURE SIZE ( $\mu\text{m}$ )	TEMP. ( $^{\circ}\text{C}$ )	PRESSURE (PSIG)	CHEMICAL (S)	TIME I (SEC.)	CHEMICAL II	TIME II (SEC.)
1	NOVOLACK-BASED	0.1 to 0.5	0.25 DIAMETER, 2.0 DEPTH	60	3500	1 mL MIXTURE OF A TERTIARY AMINE-BASED STRIPPER WITH A KELATING AGENT AND IPA	120	2 mL IPA	30
2	NOVOLACK-BASED	0.1 to 0.5	0.25 DIAMETER, 2.0 DEPTH	60	3500	1 mL MIXTURE OF A 3 TO 6 CARBON DIONE AND NMP	60	2 mL IPA	30
3	NOVOLACK-BASED	0.1 to 0.5	0.25 DIAMETER, 2.0 DEPTH	60	3500	1 mL MIXTURE OF A 3 TO 6 CARBON DIONE, NMP AND DMSO	30	1 mL IPA	30

FIG. 6

## INTERNATIONAL SEARCH REPORT

Inter. Application No  
PCT/US 00/22454

## A. CLASSIFICATION OF SUBJECT MATTER

IPC 7 H01L21/311 H01L21/3213 H01L21/306 G03F7/42 B08B7/00

According to International Patent Classification (IPC) or to both national classification and IPC

## B. FIELDS SEARCHED

Minimum documentation searched (classification system followed by classification symbols)  
IPC 7 H01L G03F B08B

Documentation searched other than minimum documentation to the extent that such documents are included in the fields searched

Electronic data base consulted during the international search (name of data base and, where practical, search terms used)

EPO-Internal, PAJ, WPI Data, INSPEC

## C. DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	WO 99 49998 A (DAVENHALL LEISA B ;UNIV CALIFORNIA (US)) 7 October 1999 (1999-10-07) the whole document ---	1,3-8, 10-14
A		15,40-42
X	RUBIN J B ET AL: "COMPARISON OF CHILLED DI WATER/OZONE AND CO <sub>2</sub> -BASED SUPERCRITICAL FLUIDS AS REPLACEMENTS FOR PHOTORESIST-STRIPPING SOLVENTS" IEEE/CPMT INTERNATIONAL ELECTRONICS MANUFACTURING TECHNOLOGY SYMPOSIUM, US, NEW YORK, NY: IEEE, 19 October 1998 (1998-10-19), pages 308-314, XP000849790 ISBN: 0-7803-4524-X page 310 -page 311 ---	1,3,8,10

Further documents are listed in the continuation of box C.

Patent family members are listed in annex.

## \* Special categories of cited documents :

- \*A\* document defining the general state of the art which is not considered to be of particular relevance
- \*E\* earlier document but published on or after the international filing date
- \*L\* document which may throw doubts on priority claim(s) or which is cited to establish the publication date of another citation or other special reason (as specified)
- \*O\* document referring to an oral disclosure, use, exhibition or other means
- \*P\* document published prior to the international filing date but later than the priority date claimed

- \*T\* later document published after the international filing date or priority date and not in conflict with the application but cited to understand the principle or theory underlying the invention
- \*X\* document of particular relevance; the claimed invention cannot be considered novel or cannot be considered to involve an inventive step when the document is taken alone
- \*Y\* document of particular relevance; the claimed invention cannot be considered to involve an inventive step when the document is combined with one or more other such documents, such combination being obvious to a person skilled in the art.
- \*&\* document member of the same patent family

Date of the actual completion of the international search

25 January 2001

Date of mailing of the International search report

01/02/2001

Name and mailing address of the ISA

European Patent Office, P.B. 5818 Patentlaan 2  
NL - 2280 HV Rijswijk  
Tel. (+31-70) 340-2040, Tx. 31 651 epo nl,  
Fax: (+31-70) 340-3016

Authorized officer

Szarowski, A

## INTERNATIONAL SEARCH REPORT

International Application No

PCT/US 00/22454

## C.(Continuation) DOCUMENTS CONSIDERED TO BE RELEVANT

Category	Citation of document, with indication, where appropriate, of the relevant passages	Relevant to claim No.
X	US 5 866 005 A (DESIMONE JOSEPH M ET AL) 2 February 1999 (1999-02-02) claims 1,2,12-15,21,22,25,29-31 ---	1,8
A		2,4,5,9, 11,12, 15,40-42
X	EP 0 836 895 A (IBM) 22 April 1998 (1998-04-22) column 4, line 16 -column 6, line 53 ---	1,8
X	US 5 304 515 A (MORITA KIYOKAZU ET AL) 19 April 1994 (1994-04-19) column 6 -column 7, line 6 ---	1,8

## INTERNATIONAL SEARCH REPORT

Information on patent family members

Inter [REDACTED] Application No

PCT/US 00/22454

Patent document cited in search report		Publication date		Patent family member(s)	Publication date
WO 9949998	A	07-10-1999	AU	3360399 A	18-10-1999
US 5866005	A	02-02-1999	US	5783082 A	21-07-1998
			AU	7525896 A	22-05-1997
			EP	0958068 A	24-11-1999
			JP	11514570 T	14-12-1999
			WO	9716264 A*	09-05-1997
			US	5944996 A	31-08-1999
EP 0836895	A	22-04-1998	US	5908510 A	01-06-1999
			JP	10125644 A	15-05-1998
			US	5976264 A	02-11-1999
US 5304515	A	19-04-1994	JP	2035458 A	06-02-1990
			JP	2113525 A	25-04-1990
			US	5185296 A	09-02-1993